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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/560,109 Confirmation No.: 3400
First Named Inventor: Sallaway, Peter J. Filing Date: 28 April 2000
Group Art Unit: 2734 Examiner: Tran, K.
Atty. Docket No.: M-5628 US
Title: Detector For A Gigabit Ethernet Receiver
Assignee(s): National Semiconductor Corporation

Mountain View, California
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AMENDMENT

Sir:

In further supplement to the Text Amendment submitted 15 September 2003, please amend the above patent application in the following manner.

IN THE SPECIFICATION

Page 18, amend the paragraph beginning at line 33 as follows:

Receiver system 500 includes receivers 501-1 through 501-N, one for each of lines 103-1 through 103-N, respectively. Receiver 501-j, an arbitrarily chosen one of receivers 501-1 through 501-N, includes filter/digitizer 502-j, equalizer 505-j, and coefficient update 506-j. Signal $x_{k,j}$ from wire 103-j is received by filter/digitizer 502-j. ~~Signals from wire 103-j, $x_{k,j}$, are received by filter digitizer 502-j.~~ Filter/digitizer 502-j filters, digitizes and amplifies the signal $x_{k,j}$ and outputs a signal $y_{k,j}$. Equalizer 505-j receives the signal $y_{k,j}$, equalizes it to remove the effects of intersymbol interference, and outputs a signal $a_{k,j}$, which is the output signal for receiver 501-j. Filter/digitizer ~~Filter/Digitizer~~ 502-j can be arranged

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